



# 

by Sunho Kim





How does JIT work in LLVM

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clang-repl demo

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Windows COFF JITLink example

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Motivation

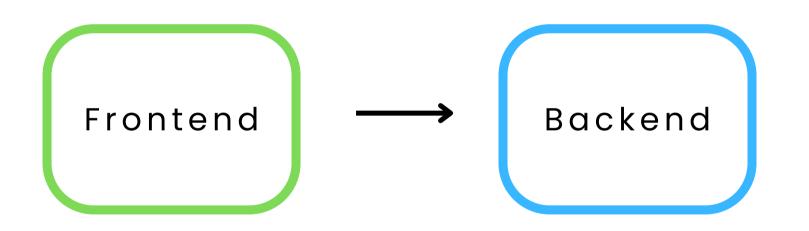
clang-repl demo

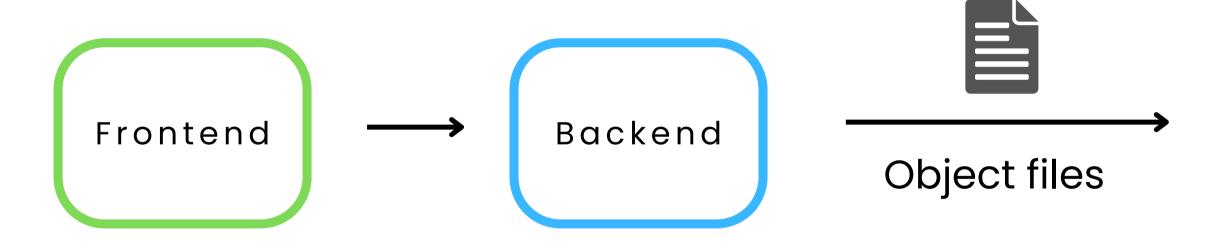
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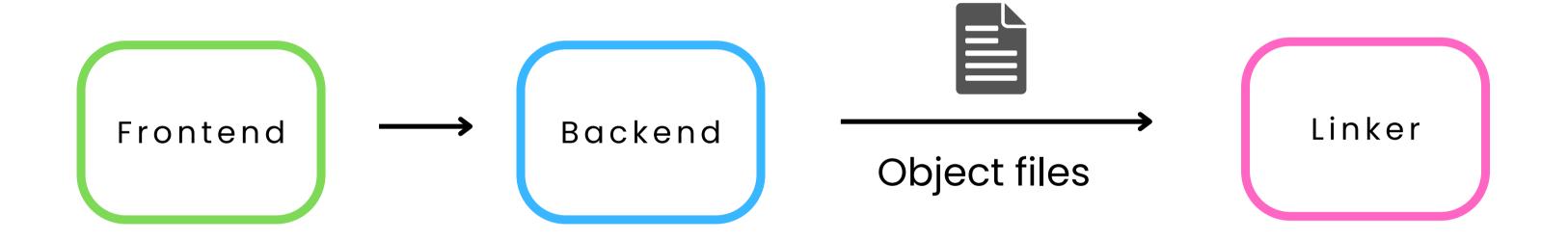
Windows COFF JITLink plugin example

Tips on using JITLink in COFF

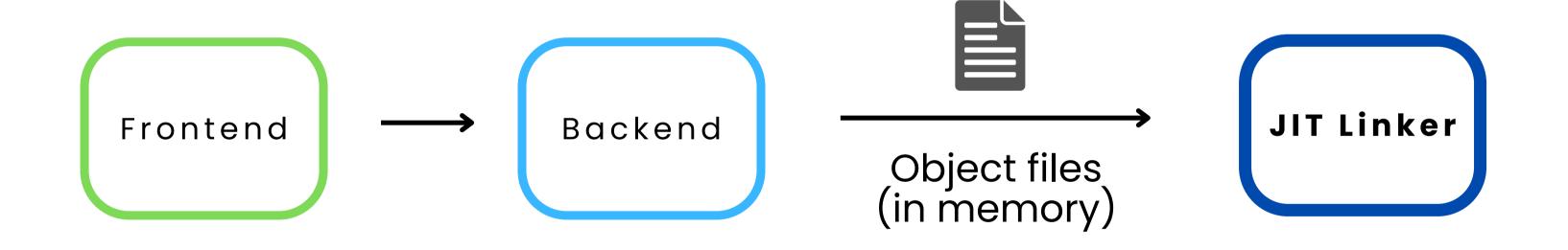




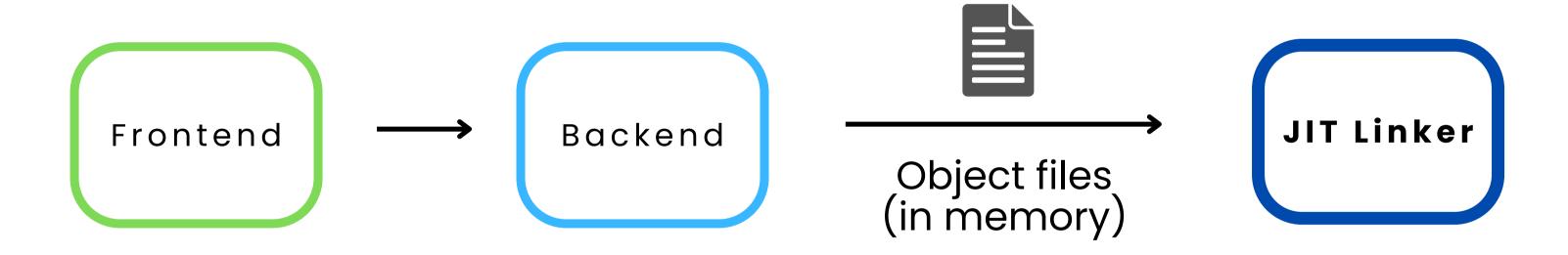




JIT execution pipeline in LLVM



JIT execution pipeline in LLVM



- Share a huge portion of pipeline with AOT
- Fewer breakage by LLVM internal code changes

#### Old JIT linker: RuntimeDyld

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#### **New JIT linker: JITLink**

 Small code model aware memory allocator

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- Runtime features fully supported including static initializers and thread local storage
- Generic linker object abstraction LinkGraph
- Easy to fully implement native object file features

• Capable of linking object files **generated by MSVC** 

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- Structured Exception Handling (SEH) support
- Incremental linking works by default

# CLANG-REPL DEMO

# **CLANG-REPL DEMO**

• clang-repl is c++ JIT interpreter devloped inside LLVM in-tree

### **CLANG-REPL DEMO**

- clang-repl is c++ JIT interpreter devloped inside LLVM in-tree
- Since it's targetting Windows COFF right now, it's MSVC compliant interactive c++ REPL

# WINDOWS COFF JITLINK EXAMPLE

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#### **LLVM IR executor**

We're going to build a simple JIT application

- Executes the **LLVM IRs** written inside main.ll using JIT
- main.ll will be generated from clang
- We're reading IRs from file for simplicity
  - IRs can be generated just in time entirely within memory

```
auto Builder = LLJITBuilder();
...
std::unique_ptr<LLJIT> J = ExitOnErr(Builder.create());
ExitOnErr(J->loadOrcRuntime("ort_rt-x86_64.lib"));
ExitOnErr(J->addIRModule(readIRModule("main.ll")));

ExecutorAddr MainAddr = ExitOnErr(J->lookup("main"));
ExitOnErr(J->initialize(J->getMainJITDylib()));

int (*Main)(void) = MainAddr.toPtr<int(void)>();
int Result = Main();
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**LLVM IR executor** 

LLJIT::loadOrcRuntime function can be used to load orc runtime into JIT session.

orc\_rt-x86\_64.lib file is inside compiler-rt build

**LLVM IR executor** 

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Loading SDL library built by MSVC into JIT session

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  - ELFLinkGraphBuilder, COFFLinkGraphBuilder, MachOLinkGraphBuilder

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- Different formats of object files: ELF, MachO, COFF
- Different architecture of binary code: x86\_64, aarch64, risc-v, ppc
- JITLink converts object file into generic linker object representation LinkGraph
  - ELFLinkGraphBuilder, COFFLinkGraphBuilder, MachOLinkGraphBuilder
- Then, it performs generic **memory allocation, symbol resolution** as described in **LinkGraph** and perform architecture-specific **relocations** as needed

### **Overview of LinkGraph**

### Block (Code)

```
mov rdi, l
mov rsi, message
jmp printf
```

### Block (Data)

"Hello, world"

Overview of LinkGraph

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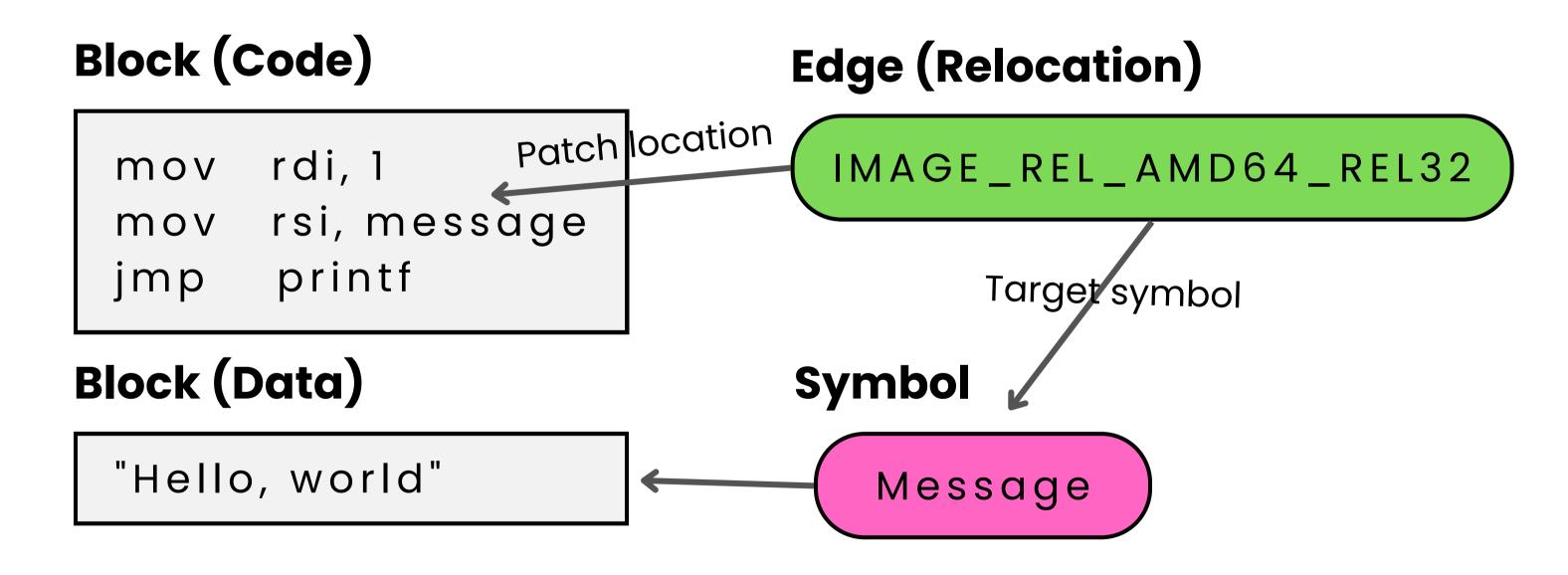
### Block (Data)

"Hello, world"

### **Symbol**

Message

Overview of LinkGraph



```
class ExamplePlugin : public ObjectLinkingLayer::Plugin {
     public:
       void modifyPassConfig(MaterializationResponsibility &MR,
 3
                             jitlink::LinkGraph &G,
 4
                             jitlink::PassConfiguration &Config) override {
 5
         Config.PrePrunePasses.push_back([&](jitlink::LinkGraph &G) {
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 7
           G.dump(llvm::outs());
           return Error::success();
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         });
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#### **Basic plugin**

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         });
 9
10
                             allocate
           dead
                                                   resolve
                                                                        apply
                           block mem
           strip
                                                                      relocation
                                                  symbols
PrePrune
                                    PostAllocation
                                                                               PostFixup
                  PostPrune
                                                             PreFixup
```

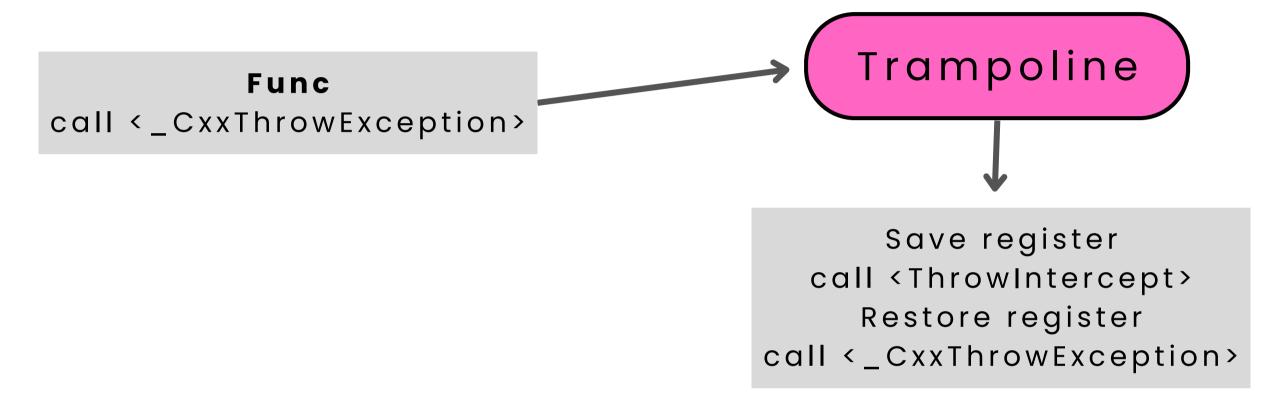
**Exception instrumentation plugin** 

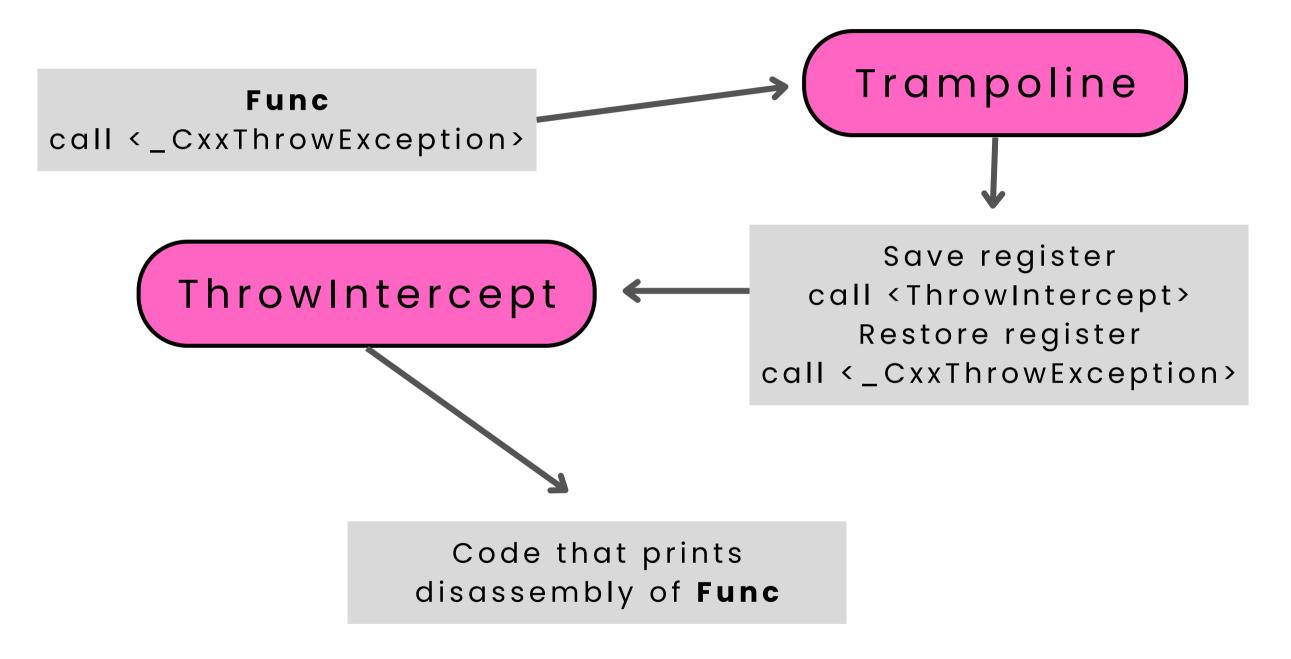
#### **Exception instrumentation plugin**

• Print the name of the function that just raised exception









```
// Create trampoline JITLink block within given LinkGraph
jitlink::Symbol *createTrampoline(jitlink::LinkGraph &G) {
   std::vector<jitlink::Edge> Edges;
   std::vector<char> CodeBuf;

// Write x86 assembly code to CodeBuf
WriteSaveRegsCode(CodeBuf);
WriteCallFuncCode(CodeBuf);
```

#### **Exception instrumentation plugin**

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### CodeBuf (content bytes of block)

```
0: pushq
              %rbp
   1: mova
              %rsp, %rbp
              $512, %rsp
   4: subq
   b: movq
             %rcx, -16(%rbp)
             %rdx, -24(%rbp)
   f: movq
   13: movq
             %rsi, -32(%rbp)
             %rdi, -40(%rbp)
   17: mo∨q
8
   6f: e8 00 00 00 callq <ThrowIntercept>
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#### **Exception instrumentation plugin**

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#### **Exception instrumentation plugin**

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                                                                            0: pushq
                                                                                         %rbp
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                                                                            1: mova
 WriteSaveRegsCode(CodeBuf);
                                                                            4: subq
                                                                                         $512, %rsp
 WriteCallFuncCode(CodeBuf);
                                                                            b: movq
                                                                                         %rcx, -16(%rbp)
 // Add relocation edge to ThrowIntercept
 auto ThrowInterceptSymbol =
                                                                            f: movq
                                                                                         %rdx, -24(%rbp)
     &G.addExternalSymbol("ThrowIntercept", 0, jitlink::Linkage::Strong);
                                                                                         %rsi, -32(%rbp)
                                                                            13: movq
 Edges.push back(jitlink::Edge(jitlink::x86 64::PCRel32, CodeBuf.size() - 4,
                           *ThrowInterceptSymbol, 0));
                                                                                         %rdi, -40(%rbp)
                                                                            17: mova
                                                                       8
                                                                            6f: e8 00 00 00 callq <ThrowIntercept>
                                                                  jitlink::x86_64::PCRel32
```

Edge

ThrowIntercept

**Symbol** 

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Config.PrePrunePasses.push_back([&](jitlink::LinkGraph &G) {
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Config.PrePrunePasses.push_back([&](jitlink::LinkGraph &G) {
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    for (jitlink::Block *Block : G.blocks()) {
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Config.PrePrunePasses.push_back([&](jitlink::LinkGraph &G) {
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    for (jitlink::Block *Block : G.blocks()) {
        for (jitlink::Edge &Edge : Block->edges()) {
```

```
Config.PrePrunePasses.push_back([&](jitlink::LinkGraph &G) {
    jitlink::Symbol *Trampoline = nullptr;
    for (jitlink::Block *Block : G.blocks()) {
        for (jitlink::Edge &Edge : Block->edges()) {
            if (Edge.getTarget().getName() == "_CxxThrowException") {
```

**Exception instrumentation plugin** 

std::map<uint64\_t, std::string> AddrToSymbolName;

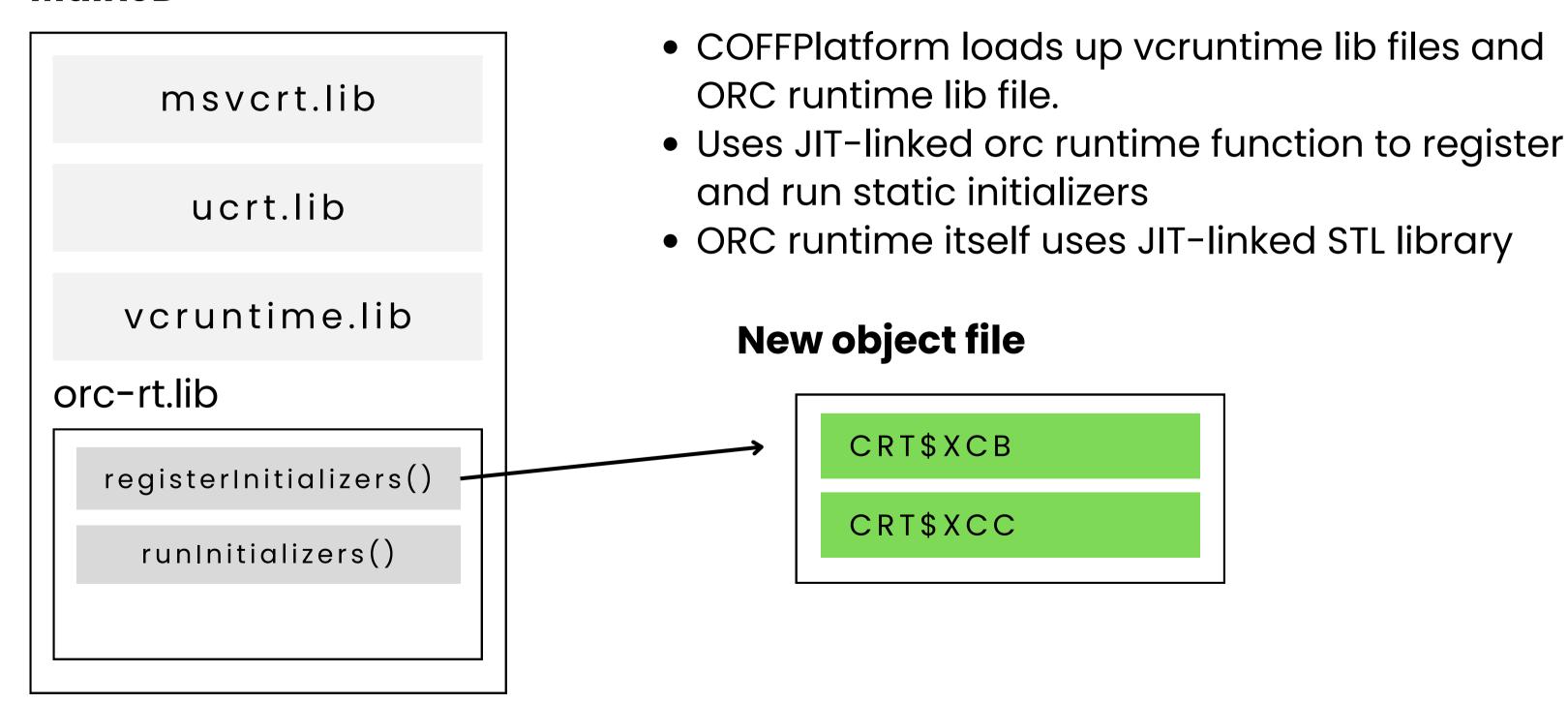
```
config.PostFixupPasses.push_back([&](jitlink::LinkGraph &G) {
  for (auto *S : G.defined_symbols()) {
    AddrToSymbolName[S->getAddress().getValue()] = S->getName();
  }
  return Error::success();
});
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#### **ORC Runtime at startup**

#### MainJD



#### **ORC Runtime at startup**

#### Tips

- Care is needed to make sure ORC and vc runtime library files are available
  - by default, vc runtime libraries automatically detected from VC toolchain directories (can fail)
- Customizing vc runtime loading can be done by COFFVCRuntimeBootstrapper class
- It is still possible to use in-process vc runtime symbols, but need to export required symbols manually by using linker directive
  - #pragma comment(linker, "/export:??\_7type\_info@@6B@")

#### JITDYLIB: Emulated DYLIB inside JIT session

#### Challenges with COFF small code model

- Compilers assume that all symbols within the same executable or dylib are allocated close together
- It is not possible to "patch" instructions to use GOT pointer on demand when the required displacement exceeds 2Gb
- COFF x86 relocation points to the middle of instruction bytes
  - x86 encoding is not possible to be read backwards to know the start of instruction (for instructions of interest because of presence of RAX prefix)
  - -> can't patch this part

#### JITDYLIB: Emulated DYLIB inside JIT session

#### **JITDYLib**

JITLink memory
manager enforces the
distance from
ImageBase to not
exceed larger amount

0x1000

0x1001

0x1053

ImageBase

"Some bytes"

HelloWorld

mov rdi, l mov rsi, message jmp printf

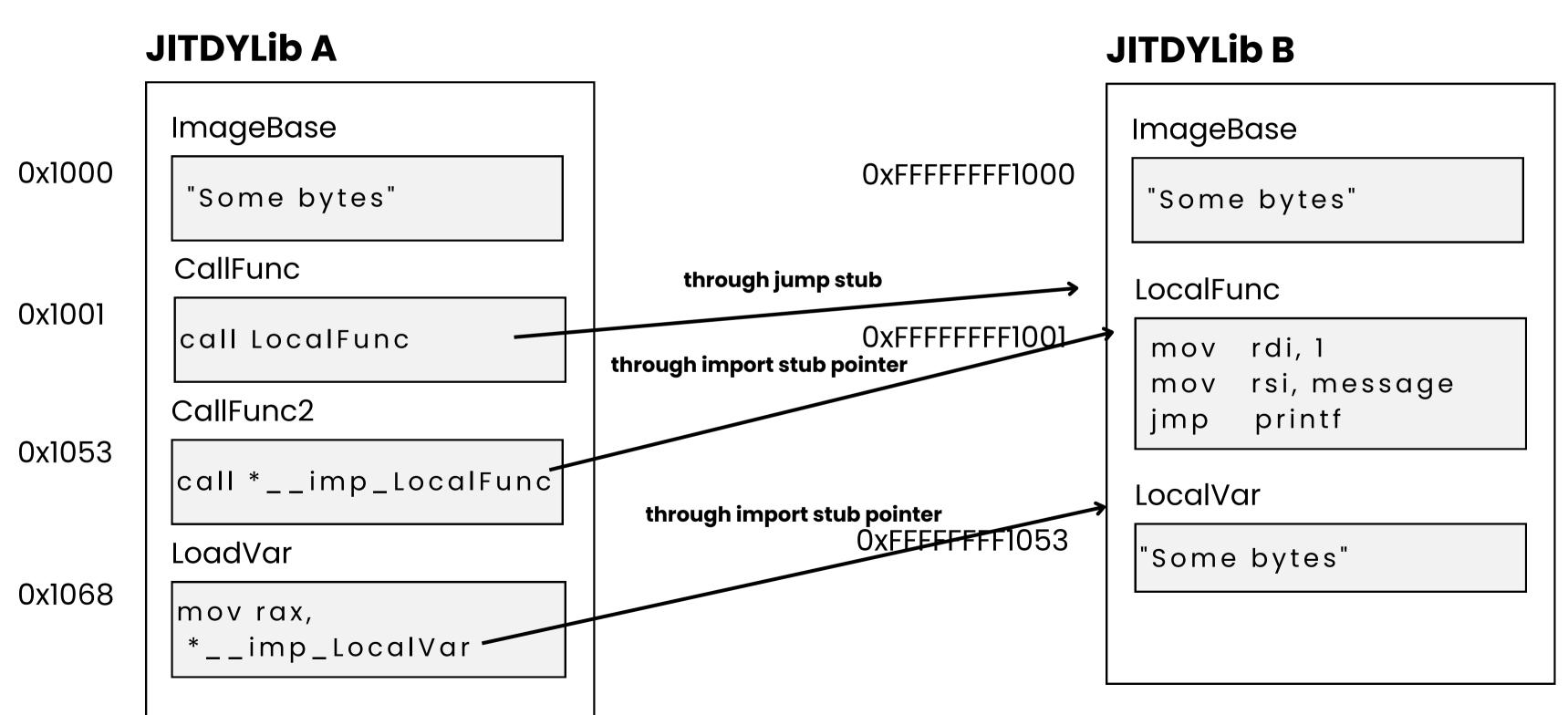
HelloWorld2

mov rdi, l mov rsi, message jmp printf

- Emulated dylib inside JIT session
- dlopen and dlclose JITDYLib inside JITted code

code linked by JITLink added

JITDYLIB: Emulated DYLIB inside JIT session



JITDYLIB: Emulated DYLIB inside JIT session

#### Tips

- Call function of another JITDYLib through usual call or dllimport attribute (\_\_imp\_)
- Access data of another JITDYLib only through dllimport attirbute (\_\_imp\_)
- Same practices are required in AOT world too but less clear in JIT world

## **THANKS**

Code discussed today is available at: <a href="https://github.com/sunho/LLVM-JITLink-COFF-Example">https://github.com/sunho/LLVM-JITLink-COFF-Example</a>